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<b>SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application Number	09/514,874
		Filing Date	February 28, 2000
		First Named Inventor	William H. Robertson et al.
		Group Art Unit	2182
		Examiner Name	Stacy A. Whitmore
		Attorney Docket Number	248/225
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OTHER DOCUMENTS - NON PATENT LITERATURE			
Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
AW	9	Benini, L. et al., "Distributed EDA tool integration: the PPP paradigm," Proceedings of the International Conference on Computer Design, 10/1998, IEEE, pgs. 448-453	
	10	Blanchard, D., "EDA on the WWW PCB CAD," Printed Circuit Design, Miller Freeman, 02/1998, vol. 15, no. 2, pgs. 34-37	
	11	Chan et al., "WELD - An Environment for Web-Based Electronic Design," Proceedings of the 35 <sup>th</sup> Design Automation Conference, 05/1998, pgs. 146-151	
	12	Geppert, L., "The Rising Tide of Web-Based Tools and Services Augurs the Next Sea Change in Electronic Design IC Design on the World Wide Web," IEEE Spectrum, 06/1998, vol. 35, no. 6, pgs. 45-50	
	13	Moretti, "Got IP," Printed Circuit Design, Miller Freeman, 10/1998, vol. 15, no. 10, pgs. 31-33	
	14	Pemin, B., "Web-Based Circuit Engineering," Circuit Cellar Ink, 03/1999, no. 104, pgs. 28-30, 33, 35, 37-38	
	15	Schindler, P. et al., "IP repository, a Web based IP reuse infrastructure," Custom Integrated Circuits, 05/1999, IEEE, pgs. 415-418	
	16	Kanduri et al., "A Framework for Collaborative and Distributed Web-Based Design," Proceedings of the 36 <sup>th</sup> Design Automation Conference, 06/1999, pgs. 898-903	
	17	Wilsay, P., "Web-Based Analysis and Distributed IP," Proceedings of the 1999 Winter Simulation Conference, pgs. 1445-1453	
	18	Rao et al., "Web-Based Network Analysis and Design," ACM Transactions on Modeling and Computer Simulation, 01/2000, vol. 10, issue 1, pgs. 18-38	
AW	19	Wen et al., "Concurrent-Simulation-Based Remote IP Evaluation over the Internet for System-On-Chip Design," 14 <sup>th</sup> International Symposium on System Synthesis, 08/2001, pgs. 233-238	

Examiner Signature		Date Considered	2/26/04
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\* EXAMINER: (initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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